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Robert John (Bob) Widlar was an American electrical engineer and a pioneering designer of linear integrated circuits (ICs). Widlar invented the basic building blocks

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Fairchild semiconductor corporation panel

Employees of Fairchild Semiconductor reminisce on their experiences in the development and marketing of the company's early linear (analog) integrated circuit

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Fairchild semiconductor datasheets for ic linear

IC Linear Voltage Regulators: Integrated circuit (IC) linear voltage regulators use voltage-controlled sources to force a fixed voltage to appear at their output

Patent us4012734 - synchronous power communicating

FIG. 18 shows specific interconnections among integrated circuits forming which is a Fairchild Semiconductor 741 by J. N. Giles, Linear Integrated Circuits

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In J. N. Giles (Ed.), Linear integrated circuits applications handbook. Mountain View, Calif: Fairchild
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the A702 designed by Bob Widlar at Fairchild Semiconductor, Op Amp Applications Handbook Op Amps and Linear Integrated Circuits; James M. Fiore

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Peter Sprague. Save. Peter Sprague (Peter Tripp Sprague, born October 11, 1955) is a critically acclaimed American jazz guitarist, composer, musical arranger and

Patent us3742361 - threshold extension phase

described in James N. Giles, Fairchild Semiconductor Linear Integrated Circuits Applications Handbook N. Giles, Fairchild Semiconductor Linear

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Comparator for driving DTL and TTL integrated circuits in many linear applications speed is not a problem and the lower input R. J. Widlar and J. N. Giles

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FIG. 1 is a block diagram of the basic circuit of the in Fairchild Semiconductor Linear Integrated Circuits Applications Handbook by James N. Giles

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in Semiconductor Technology, integrated circuits Semiconductor, James was the applications support for linear products at

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